

Application Number 10/573717  
Response to the Office Action dated 12/14/2007

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**Amendments to the Claims:**

This listing of claims will replace all prior versions and listings of claims in the application.

**Listing of Claims:**

1. (Currently Amended) A method for manufacturing a component built-in module, comprising the steps of:

on one principal surface of a first electrical insulating sheet with a cavity formed penetrating therethrough, laminating a second electrical insulating sheet so as to cover the cavity, so that a third electrical insulating sheet comprising the first electrical insulating sheet and the second electrical insulating sheet is formed;

forming a via hole so as to penetrate through the third electrical insulating sheet;  
filling the via hole with a conductive resin paste;

disposing a first wiring board with a first wiring pattern and an electronic component mounted on the first wiring pattern so as to face a principal surface of the third electrical insulating sheet at which the cavity has been formed, and disposing a second wiring board with a second wiring pattern so as to be opposed to the first wiring board with respect to the third electrical insulating sheet;

stacking the first wiring board, the third electrical insulating sheet and the second wiring board so that the electronic component is built in the cavity and the via hole is disposed between the first wiring pattern and the second wiring pattern; and

applying heat and pressure by hot pressing to the stacked first wiring board, third electrical insulating sheet and second wiring board so that a shape of the cavity is altered to coincide with outer dimensions of the mounted electronic component, so that the electronic component is fixed in the cavity and the first wiring pattern and the second wiring pattern are connected electrically through a via conductor made of the conductive resin paste.

Application Number 10/573717  
Response to the Office Action dated 12/14/2007

2. (Original) The method for manufacturing a component built-in module according to claim 1,

wherein the third electrical insulating sheet is formed by further laminating a fourth electrical insulating sheet with a cavity formed penetrating therethrough so as to be opposed to the first electrical insulating sheet with respect to the second electrical insulating sheet,

the second wiring board disposed to be opposed to the first wiring board further comprises an electronic component mounted on the second wiring pattern, and

the first wiring board, the third electrical insulating sheet and the second wiring board are stacked so that the electronic component mounted on the second wiring pattern is built in the cavity formed in the fourth electrical insulating sheet.

3. (Original) The method for manufacturing a component built-in module according to claim 1,

wherein the second electrical insulating sheet comprises a cavity penetrating therethrough, the cavity being formed at a position that does not overlap with a position of the cavity formed in the first electrical insulating sheet,

the second wiring board disposed to be opposed to the first wiring board further comprises an electronic component mounted on the second wiring pattern, and

the first wiring board, the third electrical insulating sheet and the second wiring board are stacked so that the electronic component mounted on the second wiring pattern is built in the cavity formed in the second electrical insulating sheet.

4. (Original) The method for manufacturing a component built-in module according to claim 1,

wherein the first wiring board further comprises a supporting member with the first wiring pattern formed thereon, and

after applying heat and pressure by the hot pressing, the supporting member is removed.

Application Number 10/573717  
Response to the Office Action dated 12/14/2007

5. (Original) The method for manufacturing a component built-in module according to claim 1,

wherein the cavity formed in the first electrical insulating sheet comprises a first cavity and a second cavity,

the second electrical insulating sheet comprises a third cavity communicating with the second cavity,

the electronic component mounted on the first wiring pattern of the first wiring board comprises a first electronic component and a second electronic component whose height is larger than that of the first electronic component, and

the first wiring board, the third electrical insulating sheet and the second wiring board are stacked so that the first electronic component is built in the first cavity and the second electronic component is built in the second cavity and the third cavity.

6. (Original) The method for manufacturing a component built-in module according to claim 1, wherein the first electrical insulating sheet and the second electrical insulating sheet comprise 70 to 95 weight% of inorganic filler and 5 to 30 weight% of uncured thermosetting resin composition.

7. - 8. (Canceled)

9. (Original) The method for manufacturing a component built-in module according to claim 1, wherein after attaching a protective film to a principal surface of the third electrical insulating sheet, the via hole is formed so as to penetrate through the protective film and the third electrical insulating sheet.

10. (Original) The method for manufacturing a component built-in module according to claim 1,

wherein the via hole is filled with the conductive resin paste by the steps of:

(i) disposing the conductive resin paste on a principal surface of the third electrical insulating sheet;

Application Number 10/573717  
Response to the Office Action dated 12/14/2007

(ii) applying the conductive resin paste over the principal surface so that a paste layer made of the conductive resin paste with a predetermined thickness is formed around an opening of the via hole on the principal surface, while filling the via hole with the conductive resin paste; and

(iii) scraping the paste layer off from the principal surface, while filling the via hole with the conductive resin paste.

11. (Original) The method for manufacturing a component built-in module according to claim 10, wherein the paste layer is formed on the opening of the via hole and at a region within at least 300  $\mu\text{m}$  from an edge of the opening.

12. (Previously Presented) The method for manufacturing a component built-in module according to claim 10, wherein the predetermined thickness of the paste layer is 10  $\mu\text{m}$  or more and 100  $\mu\text{m}$  or less.

13. (Previously Presented) The method for manufacturing a component built-in module according to claim 10, wherein before conducting the step (iii), a part of a resin component of the conductive resin paste charged in the via hole is sucked through an opposite opening of the via hole.

14. (Canceled)

15. (Original) The method for manufacturing a component built-in module according to claim 10, wherein the step (iii) is conducted while sucking a part of a resin component of the conductive resin paste charged in the via hole through an opposite opening of the via hole.

16. (Original) The method for manufacturing a component built-in module according to claim 10, wherein after conducting the step (ii) plural times, the step (iii) is conducted.

Application Number 10/573717  
Response to the Office Action dated 12/14/2007

17. (Original) The method for manufacturing a component built-in module according to claim 1,

wherein after attaching a protective film to a principal surface of the third electrical insulating sheet, the via hole is formed so as to penetrate through the protective film and the third electrical insulating sheet; and

the via hole is filled with the conductive resin paste by the steps of: (i) disposing the conductive resin paste on a principal surface of the protective film; (ii) applying the conductive resin paste over the principal surface of the protective film so that a paste layer made of the conductive resin paste with a predetermined thickness is formed around an opening of the via hole on the principal surface of the protective film, while filling the via hole with the conductive resin paste; and (iii) scraping the paste layer off from the principal surface of the protective film, while filling the via hole with the conductive resin paste.

18. (Original) The method for manufacturing a component built-in module according to claim 17, wherein the paste layer is formed on the opening of the via hole and at a region within at least 300  $\mu\text{m}$  from an edge of the opening.

19. (Previously Presented) The method for manufacturing a component built-in module according to claim 17, wherein the predetermined thickness of the paste layer is 10  $\mu\text{m}$  or more and 100  $\mu\text{m}$  or less.

20. (Previously Presented) The method for manufacturing a component built-in module according to claim 17, wherein before conducting the step (iii), a part of a resin component of the conductive resin paste charged in the via hole is sucked through an opposite opening of the via hole.

21. (Canceled)

Application Number 10/573717  
Response to the Office Action dated 12/14/2007

22. (Original) The method for manufacturing a component built-in module according to claim 17, wherein the step (iii) is conducted while sucking a part of a resin component of the conductive resin paste charged in the via hole through an opposite opening of the via hole.

23. (Original) The method for manufacturing a component built-in module according to claim 17, wherein after conducting the step (ii) plural times, the step (iii) is conducted.

24. (Canceled)